

Atty Docket No.: JCLA8066

Serial No.: 10/065,380

In The Claims:

Please substitute the following clean copy text for the pending claims of the same number, and cancel claims 7 and 9-15 without prejudice, waiver, or disclaimer.

1. (Once Amended) A unit cell in a liquid crystal display device, the unit cell comprising:
 - a first capacitor electrode on a substrate;
 - a capacitor dielectric layer on the first capacitor electrode;
 - a second capacitor electrode on the capacitor dielectric layer, wherein the second capacitor electrode has a surface area smaller than the first capacitor electrode, to prevent overlapping with edges of the first capacitor electrode;
 - a passivation layer on the second capacitor electrode, wherein the passivation layer has an opening that exposes a portion of the second capacitor electrode; and
 - a pixel electrode layer on the passivation layer such that the pixel electrode layer and the second capacitor electrode are electrically connected through the opening in the passivation layer.
2. (Once Amended) The unit cell of claim 1, wherein an overlapping region between the first capacitor electrode and the second capacitor electrode is substantially equal to the surface area of the second capacitor electrode.
3. (Once Amended) The unit cell of claim 1, wherein the pixel electrode is further connected to a switching element.
4. (Once Amended) The unit cell of claim 1, wherein the pixel electrode is further connected to a thin film transistor.
5. (Once Amended) The unit cell of claim 1, wherein the first capacitor electrode is further connected to a common voltage.

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6. (Once Amended) A storage capacitor structure in a unit cell of a liquid crystal display device, the storage capacitor structure comprising:

a first capacitor electrode on a substrate; a capacitor dielectric layer on the substrate; and a second capacitor electrode on the capacitor dielectric layer. The second capacitor electrode are bounded within the edges of the first capacitor electrode.

8. (Once Amended) The capacitor structure of claim 6, wherein if residual conductive material is distributed along the edges of the first capacitor electrode, short-circuiting of the storage capacitor structure being prevented because no overlapping between the second capacitor electrode and the edges of the first capacitor electrode.

Please add new claims 17-18.

17. (Newly Added) A storage capacitor for holding a voltage provided from a signal line of a liquid crystal device within a predetermined interval, the storage capacitor comprising:

a first capacitor electrode disposed on a substrate of the liquid crystal device;

a second capacitor electrode disposed substantially over the first capacitor electrode electrically connected to a pixel electrode; and

dielectric means laminated between the first capacitor electrode and the second capacitor electrode;

wherein an area of the second capacitor electrode normally projected on the plane of the first capacitor electrode is substantially bounded within an area of the first capacitor electrode so as to prevent electrical short between the second capacitor electrode and the signal line.

18. (Newly Added) A storage capacitor for holding a voltage provided from a signal line of a liquid crystal device within a predetermined interval, the storage capacitor comprising:

a first capacitor electrode disposed on a substrate of the liquid crystal device and having a first area defined by the contour of the first capacitor electrode;

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a second capacitor electrode disposed substantially over the first capacitor electrode and having a second area defined by the contour of the second capacitor electrode; and

dielectric means laminated between the first capacitor electrode and the second capacitor electrode;

wherein the second area of the second capacitor electrode, with respect to a plan view thereof, is substantially within the first area of the first capacitor electrode.